

STRIDER PLATINUM SERIES ST1200-PT/ST1000-PT

The perfect balance of performance, efficiency, and quality

High efficiency with 80 PLUS Platinum certification 139mm Air Penetrator fan with intelligent semi-fanless operation FF141 magnetized fan filter included 100% modular cables 24/7 continuous power output with 40°C operating temperature

Strict ±3% voltage regulation and low ripple & noise

Dual EPS 8pin with multiple individual one-on-one PCI-E 8/6pin connectors

SPECIFICATION

SilverStone Strider Platinum

SST-ST1200-PT SST-ST1000-PT

ATX12V / EPS 12V Switching Power Supply
With Active PFC
80Plus Platinum
PS/2

This specification describes the requirements of 1000W,1200Watts switching power supply with an stretch ATX form-factor and EPS 12V, +5V standby voltage, remote on/off control, full range line input capability and forced air cooling characteristics.

1. AC INPUT

1.1 AC input requirements

The input voltage, current, and frequency requirements for continuous operation are stated below.

Table 1 AC Input Line Requirements

Parameter	Min.	Nom.	Max.	Unit
Vin(Full range)	90	100240	264	VACrms
Vin Frequency	47	6050	63	Hz
lin(1000W)		126		Arms

Table 2 AC Input Line Requirements

Parameter	Min.	Nom.	Max.	Unit
Vin(Full range)	105	115240	264	VACrms
Vin Frequency	47	6050	63	Hz
lin(1200W)		12.66 or 126		Arms

Power factor correction (PF)>0.90 at full load.

The power supply must meet inrush requirements for any rated AC voltage, during turn on at any phase of AC voltage, during a single cycle AC dropout condition, during repetitive ON/OFF cycling of AC, and over the specified temperature range (Top). The peak inrush current shall be less than the ratings of its critical components (including input fuse, bulk rectifiers, and surge limiting device).

2. DC OUTPUT

2.1 DC voltage regulation

Parameter	Range	Min	Nom.	Max	Unit
+3.3V	+/-3%	+3.20	+3.30	+3.39	Volts
+5V	+/-3%	+4.85	+5.00	+5.15	Volts
+12V	+/-3%	+11.64	+12.00	+12.36	Volts
-12V	+/-10%	-13.2	-12.00	-10.8	Volts
+5VSB	+/-5%	+4.75	+5.00	+5.25	Volts

Note:1>Voltage tolerance is requied at main connector and S-ATA connector(if used)

2.2 Load ranges

2.2.1:(ST1000-PT Load Range)

Parameter	Min(optional)	Nom.	Max	Peak	Unit
+3.3V	0.1	-	25		Amps
+5V	0.2	-	22		Amps
+12V	0.15	-	83		Amps
-12V	0	-	0.3		Amps
+5VSB	0	-	2.5		Amps

- 1.Maximum continuous total DC output power should not exceed 1000W.
- 2.Maximum continuous combined load on +3.3V and +5V outputs shall not exceed 120W.
- 3.Maximum combined current for the +12V outputs shall be 83A(996W).
- 4.Peak DC output power should not exceed 1050W for 12 second.

2.2.2:(ST1200-PT Load Range)

Parameter	Min(optional)	Nom.	Max	Peak	Unit
+3.3V	0.1	-	25		Amps
+5V	0.2	-	22		Amps
+12V	0.15	-	100		Amps
-12V	0	-	0.3		Amps
+5VSB	0	-	2.5		Amps

- 1.Maximum continuous total DC output power should not exceed 1200W.
- 2.Maximum continuous combined load on +3.3V and +5V outputs shall not exceed 120W.
- 3.Maximum combined current for the +12V outputs shall be 100A(1200W).
- 4.Peak DC output power should not exceed 1250W for 12 second.

2.3 Output Ripple

2.3.1 Ripple regulation

Parameter	Ripple&Noise	Unit
+3.3V	50	mVp-p
+5V	50	mVp-p
+12V	120	mVp-p
-12V	120	mVp-p
+5VSB	50	mVp-p

2.3.2 Definition

The ripple voltage of the outputs shall be measured at the pins of the output connector when terminated in the load impedance specified in figure 1. Ripple and noise are measured at the connectors with a 0.1uF ceramic capacitor and a 10uF electrolytic capacitor to simulate system loading. Ripple shall be measured under any condition of line voltage, output load, line frequency, operation temperature.

2.3.3 Ripple voltage test circuit

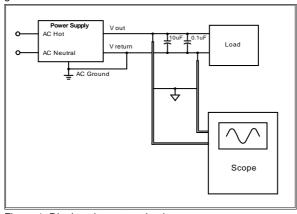


Figure 1. Ripple voltage test circuit

2.4 Overshoot

Any overshoot at turn on or turn off shall be less 10% of the norminal voltage value, all outputs shall be within the regulation limit of section 2.0 before issuing the power good signal of section 5.0.

2.5 Efficiency

Power supply efficiency typical 90% / 20% Load, 92% / 50% Load, 89% / 100% Load at normal AC main voltage.

2.6 Remote ON/OFF control

When the logic level "PS-ON" is low, the DC outputs are to be enabled. When the logic level is high or open collector, the DC outputs are to be disabled.

3. PROTECTION

3.1 Over current protection

The power supply shall have current limit to prevent the +3.3V,+5V,and +12V outputs from exceeding the values shown in the following Table. If the current limits are exceeded the power supply shall shutdown and latch off.

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Voltage	Over Current Limit (lout limit)
+12V	90A minimum; 130A maximum
+5V	25A minimum; 50A maximum
+3.3V	25A minimum; 50A maximum

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Voltage	Over Current Limit (Iout limit)	
+12V	120A minimum; 150A maximum	
+5V	25A minimum; 50A maximum	
+3.3V	25A minimum; 50A maximum	

3.2 Over Temperature Protection

The power supply will be protected against over temperature conditions caused by loss of fan cooling or excessive ambient temperature. In an OTP condition the PSU will shutdown. When the power supply temperature drops to within specified limits, the power supply shall restore power automatically. The OTP circuit must have built in hysteresis such that the power supply will not oscillate on and off due to temperature recovering condition.

3.3 Over-power protection

The power supply will be shutdown and latch off when output power within 105~150% of rated DC output. Note: Assurance machine can work at low voltage, full load won't damage machine.

3.4 Under voltage protection.

In an under voltage fault occurs, the supply will latch all DC outputs into a shutdown state when +12V, +5V & +3.3V outputs under 85% of it's maximum value.

3.5 Over voltage protection

The over voltage sense circuitry and reference shall reside in packages that are separate and distinct from the regulator control circuity and reference. No single point fault shall be able to cause a sustained over voltage condition on any or all outputs. The supply shall provide latch-mode over voltage protection as defined in Table.

output	Minimum	Nominal	Maximum	Unit
+12 VDC	13	15	17	Volts
+5 VDC	5.5	6	7	Volts
+3.3 VDC	3.6	4	5	Volts

3.6 Short circuit

An output short circuit is defined as any output impedance of less than 0.1 ohms. The power supply shall shut down and latch off for shorting the +3.3 VDC,+5 VDC,or+12 VDC rails to return or any other rail. Shorts between main output rails and +5VSB shall not cause any damage to the power supply. The power supply shall either shut down and latch off or fold back for shorting the negative rails.+5VSB must be capable of being shorted indefinitely, but when the short is removed, the power supply shall recover automatically or by cycling PS_ON#. The power supply shall be capable of withstanding a continuous short-circuit to the output without damage or overstress to the unit

3.7 No load operation

No damage or hazardous condition should occur with all the DC output connectors disconnected from the load. The power supply may latch into the shutdown state.

4. TIMING

4.1 Signal timing drawing

Figure 2. is a reference for signal timing for main power connector signals and rails.

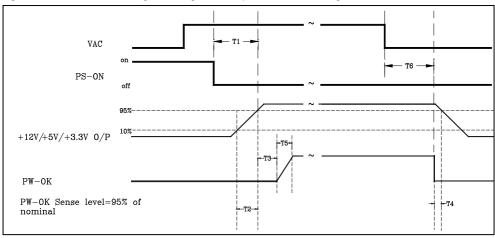


Figure 2. PS-OK Timing Sequence

- (1)T3: Power good signal turn on delay time (100ms~500ms)
- (2)T4: Power good signal turn off delay time (75%Load 1ms min)
- (3)T2: Rise time (0.1~20ms)
- (4)T6: Hold up time (75%Load 16ms)

4.2 Hold up time

When the power loss its input power, it shall maintain 16ms in regulation limit at normal input voltage and 75% full load (AC:115V/60Hz or 230V/50Hz)

5. ENVIRONMENT

5.1 Operation

Temperature	0 to 40°C
Relative Humidity	10 to 90%, non-condensing

5.2 Shipping and Storage

Temperature	-20 to 90°C
Relative Humidity	5 to 95%, non-condensing

5.3 Altitude

Operating	10,000FT max
Storage	50,000FT max

6. SAFETY

6.1 Underwriters Laboratory (UL) recognition.

The power supply designed to meet UL 60950.

7. ELECTROMAGNETIC COMPATIBILITY (EMC)

- 7.1 ELECTROSTATIC DISCHARGE (ESD) IEC 61000 4 2 : 2008
- 7.2 ELECTRICAL FAST TRANSIENT / BURST (EFT/B) IEC 61000 4 4 : 2012
- 7.3 SURGE IEC 61000 4 5 : 2005
- 7.4 POWER FREQUENCY MAGNETIC FIELD IEC 61000 4 8: 2009
- 7.5 VOLTAGE DIPS IEC 61000 4 11 : 2004
- 7.6 RADIATED SUSCEPTIBILTY IEC 61000 4 3 : 2006+A1 : 2007+A2 : 2010
- 7.7 CONDUCTED SUSCEPTIBILTY IEC 61000 4 6 : 2008
- 7.8 VOLTAGE FLUCTATION EN 61000 3 3 : 2008
- 7.9 EN61000-3-2: 2006+A2: 2009 harmonic current emissions.

If applicable to sales in Europe, the power supply shall meet the requirements of EN 61000-3-2 Class D and the Guidelines for the Suppression of Harmonics in Appliances and General Use Equipment Class D for harmonic line current content at full-rated power.

8. MTBF

8.1 MTBF (mean time between failures) calculation

The demonstrated MTBF shall be 100,000 hours of continuous operation at 25oC,at 80% load, and nominal line. The MTBF of the power supply be calculated in accordance with MIL-HDBK-217F. The DC FAN is not included.

9. Mechanical Specification

150 mm (W) x 86 mm (H) x 180mm (D)

